

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): **Van der Wagt, J.** Docket No.: **INPHI-P065**
Serial No.: (Not yet assigned) Group Art Unit: (Not yet assigned)
Filed: **7/2/03** Examiner: (Not yet assigned)
Title: **Improved High-Speed Output Buffer**

INFORMATION DISCLOSURE STATEMENT

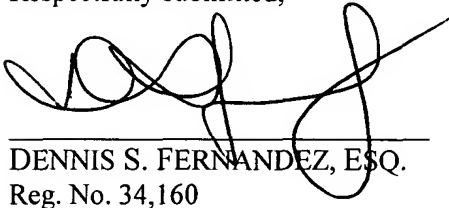
The Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In keeping with the duty of candor and good faith owed to the Patent and Trademark Office, Applicants wish to bring the enclosed printed publications and U.S. patent references to the attention of the Examiner.

Applicant respectfully submits that Applicants' claimed invention is patentable over such references. Forms PTO/SB/08A and PTO/SB/08B listing these references are attached. It is understood the Office may find additionally relevant material in these or other references

Respectfully submitted,


DENNIS S. FERNANDEZ, ESQ.
Reg. No. 34,160

7/2/03
Date

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Substitute for form 1449/PTO

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Completeness if $K_n \supseteq W_n$

Application Number	Not Yet Assigned
Filing Date	7/2/03
First Named Inventor	Van der Wagt, J.
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	INPHI-P065

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Examiner Signature		Date Considered	
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	Not Yet Assigned
Filing Date	7/2/03
First Named Inventor	Van der Waagt, J.
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	INPHI-P065

Sheet

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of

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1.	E.M. Cherr, M. Sc. Ph.D., & D. E. Hooper, M.E. Graduates, PROCEEDINGS I.E.E. Vol. 110, No. 2,	
		Feb. 1963, "The design of wide-band transistor feedback amplifiers".	
	2.	20-Gb/s Digital SSI's using AlGaAs/GaAs Heterojunction Bipolar Transistors for Future	
		Optical Transmission Systems, Haruhiko Inchino, Member, IEEE, 1993	

Examiner Signature		Date Considered	
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